

SHEET INDEX		SHEET NO.	SHEET ISSUE
CONTENTS			
SHEET INDEX			
RECORD OF CHANGES			
SUPPORTING INFORMATION			
CURRENT DRAWING		1	1
USED-ON TABLE			
NOTES			
SYMBOL			
CIRCUIT SCHEMATIC			
COMPONENT LIST		2	1
CIRCUIT DESCRIPTION			
		3	1
CMS 160A		4	1
		5	1
CMS 145P		6	1

SYMBOL

MAINTENANCE CHANNEL SWITCH SEQUENCER
AND BIPOLAR INTERFACE (MSWS,BPI)
ELEMENT A

TEAM_MEMBER	TEAM	TEAMS	LOG
CP11	I	217	246
CP21	I	208	245
CP31	I	308	243
EMMS00	I	315	243
TOLSHS00			
EMMS01	I	215	243
LNCH00	I	315	243
NCHG00	I	216	243
NCHG01	I	214	243
NCHDSLE0			
NCHDS0	I	210	244
NCHDS1	I	107	244
NCHDS2	I	007	244
NCHDS3	I	317	244
SWRTO1			
SWRTO1	I	314	243
NCHG00	B	307	203
NCHG01	B	318	203
NCHG02	B	008	264
NCHDSFT0			
NCHDSFT0	B	108	264
NCHDSFT1	B	309	205
NCHDSFT2	B	218	203
NCHDSFT3	B	209	264
SHS001			
SHS001	B	316	263
GR0	P	000,119	201
GR0	P	200,119	202

SSP TRANSFORMERS AND INTERRUPT
TRANSFORMERS (SSP.INTT)
ELEMENT R

FORM, MC	FUNC	TEMP	LOC
FBIEN	I	210	265
FBIENP	I	310	265
INTEN	I	115	266
INTAP	I	115	266
INTEN	I	115	267
INTEN	I	014	266
INTEN	I	015	267
INTAP	I	015	266
INTEN	I	016	267
INTAP	I	016	267
INTEN	I	017	267
INTEN	I	118	268
INTEN	I	018	267
INTAP	I	117	267
INTAP	I	001	268
INTEN	I	102	268
INTEN	I	002	268
INTUN	I	101	268
INTUN	I	003	268
INTEN	I	301	269
INTAP	I	301	269
MBFN	I	202	269
MBFP	I	202	269
FBI, LINFO	#	211	255
FBI, LINFO	#	311	265
MBFI	#	211	205
TS, L580	#	012	266
TS, L580	#	112	266
TS, L580	#	010	267
TS, L580	#	312	267
TS, L580	#	109	267
TS, L580	#	009	267
TS, L580	#	111	268
TS, L580	#	011	268
TS, L580	#	110	268
TS, L580	#	212	268

CURRENT ORAIN: 600 MB

RECORD OF CHANGES

ONG ISS	PREV FURN	STD	MFR DIS~	SEE NOTE

NOTES:

1. 1 GROUND RETURN
2. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS
CAPACITANCE VALUES ARE IN MICROFARADS
VALUES PRECEDED BY THE SYMBOL + (PLUS)
OR - (MINUS) ARE IN VOLTS

- ### 3. BATTERY AND GROUND TERMINALS FOR INTEGRATED CIRCUITS

[illegible]

- 4 BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL
+3V	000, 119
GRD	200, 319, 260

5. HORIZONTAL MOUNTING CENTERS AT 9.5 INCH.

SYSTEM USED ON	DESIGN CONTROL
COMMON SYSTEMS	IN

SUPPORTING INFORMATION

CATEGORY	NO.
CONNECTOR ON FRAME	947A OR 947C
SERIES FOR LATEST CLASS "A" CHANGE (ANY HIGHER SERIES IS ACCEPTABLE)	2

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE FIRST SHEET.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE FIRST SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

1496

FC202 CIRCUIT PACK
MAINTENANCE CHANNEL
AND SSP INTERFACE
CIRCUIT

AT&T
STANDARD

CPS-FC202
6 SHEETS

BELL TELEPHONE LABORATORIES

②

MAINTENANCE CHANNEL AND SSP INTERFACE CIRCUIT

COMPONENT LIST

DESIG

599

(2) C1,C2 KS-19774 L1,220pF

G I O D E

ent

4200

OFFICIAL	CODE
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DESIG

R3

R6.0, R6.1
R7.0, R7.1

R7.0, R7.1	▼	,100
R8.0-R8.10	RF-11/15, 12, 100	

R8.0-R8.10 KS-16645 L2,100

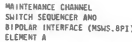
DESIG

T2

2664C

DESIG

CIRCUIT DESCRIPTION



SSP
TRANSFORMERS
AND INTERRUPT
TRANSFORMERS
(SSP.INTT)
ELEMENT 8

PART OF CMS 160A

SUPPORTING INFORMATION

CATEGORY	NO.
INTEGRATED CIRCUIT COP	160A
SERIES FOR LATEST CLASS "A" CHANGE (ANY HIGHER SERIES IS ACCEPTABLE)	5

NOTES:

- BATTERY AND GROUND TERMINALS FOR INTEGRATED CIRCUITS.

IC CODE	BAT. TERM.	GRO. TERM.
137A	25	11
137C	25	11
137D	25	11
137F	25, 26	11, 12
137H	25	11
137J	25	11
137N	25	11
137P	25	11
137Q	25	11
137R	25	11
137S	25	11
137T	25	11
137U	25	11
137V	25	11
137W	25	11
137X	25	11
137Y	25	11
137Z	25	11

160A IC

FC202 CIRCUIT PACK

BELL TELEPHONE LABORATORIES
HUNTSVILLE

CPS-FC202

SHEET 3

65

CPS-FC202

CPS-FC202



PART OF CMS 160A

COMPONENT LIST INTEGRATED CIRCUIT

LOC CODE ELEM ID	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	LOC CODE ELEM ID
A	RDYGO	304	SM1T1	384	SHS000	302	SHS000	301	RR0T0	306	SHDLY	3E1	MSDATA	AC2	NGB	304	LMCRST1	307	A
B									GR1	3E4							LMCRST0B	3E8	B
C									RSTSHS000	3C3							LMCRST0A	3E7	C
D									CP10	386							PCWST0	3E6	D
E									P11	3C6							WRESETO	3C7	E
F									SR0V1	385							SDATA1	402	F
G									SR0T0	3C5							WPD	387	G
H									SHS0001	3C1							WRESE1	387	H

LOC CODE ELEM ID	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	LOC CODE ELEM ID
A									A
B									B
C									C
D									D
E									E
F									F
G									G
H									H

LOC CODE ELEM ID	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	DESIG	SH LOC	LOC CODE ELEM ID
A	S0B	306	SDATA300	4E3	DATA300	4E2	PCHS0PD	403	CP30	301	TS130	403	SHFT	4C7	STACT0	401	A
B			SDATA200	4E4	DATA200	4E2	PCHS0MD	402	RST131	405	SHDT000	4E6			STACT1	4C1	B
C			SDATA310	4E5	DATA310	4E3			RST21	405	ED000	407			SDATA0	4C0	C
D			SDATA210	4E4	DATA210	4E1			P21	3C1	RSTSHFT1	406			PRDATA	400	D
E									CP20	408	RSTSHFT0	4E6			SDATA1	4C0	E
F															PCW000	400	F
G									SDATA0	400							G
H																	H

CAPACITORS
DESIG. CODE
C1, C2 801A, 3000F

NOTES:
1. CAPACITOR CHIP 801A, 3000pF, IS BONDED ON EACH SIDE OF 137T SIC.

160A IC

FC202 CIRCUIT PACK

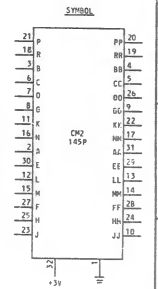
2

CPS-FC202

SHEET 5

BELL TELEPHONE LABORATORIES

REPRODUCED BY



1. BATTERY AND GROUND TERMINALS FOR INTEGRATED CIRCUITS.

IC CODE	BAT. TERM.	GRO TERM.
137H	25	11

CIRCUIT DESCRIPTION

SUPPORTING INFORMATION	
CATEGORY	NO.
INTEGRATED CIRCUIT	145P
SERIES FOR LATEST CLASS "A" CHANGE (ANY HIGHER SERIES IS ACCEPTABLE)	

BELL TELEPHONE LABORATORIES